

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L47	0	(voltage generator AND non-volatile memory AND data register AND processing circuit AND semiconductor substrate AND integrated circuit).clm.	US-PGPUB; USPAT	ADJ	ON	2005/09/24 20:08

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	9	voltage generat\$3 and (voltage with control data with data register\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/24 18:36
L2	487	stor\$3 and (control data with ((non adj volatile storage) or (non adj volatile memory)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/24 17:49
L3	1	l1 and l2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/24 17:58
L6	298	controller\$1 and semiconductor substrate and voltage generat\$3 and non-volatile memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/24 18:01
L7	1	determin\$3 and (relationship near2 deciding reference voltage with generated voltage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/24 18:02
L8	1	compar\$3 and (deciding reference voltage with generated voltage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/24 18:28
L9	848	compar\$3 and (reference voltage with generated voltage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/24 18:03
L10	8	l6 and l9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/24 18:06

L11	1	I10 and I2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/24 18:50
L12	6367	determin\$3 and (control data with program)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/24 18:04
L13	1	I10 and I12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/24 18:04
L14	0	(voltage generation AND non-volatile memory AND processing circuit AND controller AND decision circuit AND semiconductor integrated circuit). clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/24 18:40
L15	154856	central processing unit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/24 18:40
L16	119	I15 and I6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/24 18:40
L17	3	I16 and I9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/24 18:47
L18	1	I17 and I1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/24 18:41

L19	13411	714/724 or 714/819 or 714/736 or 714/725 or 324/765 or 324/771 or 365/201	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/24 18:56
L20	15	I19 and I16	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/24 19:19
L21	17121	decision circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/24 18:57
L22	1470790	controller\$1 or central processing unit or central processing circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/24 18:58
L23	3407	I21 and I22	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/24 18:58
L25	23	I6 and I23	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/24 19:02
L26	92390	semiconductor integrated circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/24 18:59
L28	1	I25 and I19	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/24 19:00

L32	848	compar\$3 and (generated voltage with reference voltage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/24 19:32
L33	1726	control data same data register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/24 19:32
L34	3	I32 and I33	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/24 19:32
L35	30850	updat\$3 and (control data)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/24 19:33
L36	3	I34 and I35	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/24 19:45
L37	432	stor\$3 and (control data with non-volatile memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/24 19:33
L38	1	I36 and I37	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/24 19:42
L39	5741	365/233	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/24 19:42

L40	10652	clock generation	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/24 19:43
L41	94	compar\$3 and (generated clock signal with reference clock signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/24 19:44
L42	20	l40 and l41	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/24 20:01
L43	28484	oscillation circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/24 19:44
L44	1	l42 and l43	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/24 19:45
L45	20860	327/156 or 713/699 or 365/233 or 714/814 or l19	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/24 20:03
L46	1	l45 and l42	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/24 20:04